



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

g fres	In re	Application of: as Bolt, et al)	Group Art Unit: 2112
er i	7/0 ₆ 2 ₄ Lhom	Thomas Bolt, et al		Examiner: Patel, Nimesh
ul2	F Serial	Serial No.: 10/075,121		Dank, to Final Action
		Filed: Feb. 13, 2002		Reply to Final Action
	Filed:			Dated January 27, 2006, and Interview Summary
	For:	Use of the Universal Serial Bus as an)	
		Internal Architecture within IDE Disk)	
		Array)	

Reply

MS AF Commissioner for Patents P.O. BOX 1450 ALEXANDRIA, VA 22313-1450

Dear Sir/Madam:

Applicant hereby responds to the Final Action of January 27, 2006 in the above-referenced patent application. Reconsideration and allowance of the claims is respectfully requested in view of the following amendments and remarks. The undersigned has been authorized to respond to the Office Action.

The claims are reflected in the listing of the claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.